



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: AMD-H0552

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In re Application

Inventor(s): KHAN, et al.

Application No.: 10/618,514

Filed: 07/11/2003

Title: METHOD OF FABRICATING SEMICONDUCTOR DEVICE HAVING TRIPLE LDD STRUCTURE AND LOWER GATE RESISTANCE FORMED WITH A SINGLE IMPLANT PROCESS

**Commissioner for Patents**  
P.O. Box 1450  
Alexandria, VA 22313-1450

## NOTIFICATION OF FILING OF DIVISIONAL APPLICATION

Notification is hereby being made of the filing of a divisional application for this case

concurrently herewith.  
 on .....

Please direct all correspondence concerning the above-identified application to the following address:

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Respectfully submitted,

Date: May 2, 2005

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